

NOTICE OF REVISION (NOR)		1. DATE (YYMMDD) 93-11-18	Form Approved OMB No. 0704-0188
This revision described below has been authorized for the document listed.			
Public reporting burden for this collection is estimated to average 2 hours per response, including the time for reviewing instructions, searching existing data sources, gathering and maintaining the data needed, and completing and reviewing the collection of information. Send comments regarding this burden estimate or any other aspect of this collection of information, including suggestions for reducing this burden, to Department of Defense, Washington Headquarters Services, Directorate for Information Operations and Reports, 1215 Jefferson Davis Highway, Suite 1204, Arlington, VA 22202-4302, and to the Office of Management and Budget, Paperwork Reduction Project (0704-0188), Washington, DC 20503. PLEASE DO NOT RETURN YOUR COMPLETED FORM TO EITHER OF THESE ADDRESSES. RETURN COMPLETED FORM TO THE GOVERNMENT ISSUING CONTRACTING OFFICER FOR THE CONTRACT/ PROCURING ACTIVITY NUMBER LISTED IN ITEM 2 OF THIS FORM.			2. PROCURING ACTIVITY NO.
			3. DODAAC
4. ORIGINATOR	b. ADDRESS (Street, City, State, Zip Code)	5. CAGE CODE	6. NOR NO.
a. TYPED NAME (First, Middle Initial, Last) N. A.	Defense Electronics Supply Center 1507 Wilmington Pike Dayton, OH 45444-5270	67268	5962-R045-94
		7. CAGE CODE	8. DOCUMENT NO.
		67268	85030
9. TITLE OF DOCUMENT		10. REVISION LETTER	
MICROCIRCUIT, LINEAR, PERCISION VOLTAGE REFERENCES, THIN FILM, HYBRID.		a. CURRENT D	b. NEW E
11. ECP NO.		N. A.	
12. CONFIGURATION ITEM (OR SYSTEM) TO WHICH ECP APPLIES All			
13. DESCRIPTION OF REVISION			
<p>Sheet 1: Revisions ltr column; add "E". Revisions description column; add "Changes in accordance with NOR 5962-R045-94". Revisions date column; add "93-11-18". Revision level block; change "D" to "E". Rev status of sheets; For sheet 1 change "D" to "E" and for sheet 2 change "C" to "E".</p> <p>Sheet 2: Paragraph 1.2,2, Under discriptive designator for outline letter C; add "or CDIP1-T14". Revision level block; chane "C" to "E".</p>			
14. THIS SECTION FOR GOVERNMENT USE ONLY			
a. (X one)	X	(1) Existing document supplemented by the NOR may be used in manufacture.	
		(2) Revised document must be received before manufacturer may incorporate this change.	
		(3) Custodian of master document shall make above revision and furnish revised document.	
b. ACTIVITY AUTHORIZED TO APPROVE CHANGE FOR GOVERNMENT		c. TYPED NAME (First, Middle Initial, Last)	
d. TITLE Chief, Electronic Components Branch	e. SIGNATURE Kendall A. Cottongim		f. DATE SIGNED (YYMMDD) 93-11-18
15a. ACTIVITY ACCOMPLISHING REVISION DESC-ECT	b. REVISION COMPLETED (Signature) Gary Zahn	c. DATE SIGNED (YYMMDD) 93-11-18	

NOTICE OF REVISION (NOR)		1. DATE (YYMMDD) 93-08-17	Form Approved OMB No. 0704-0188
This revision described below has been authorized for the document listed.			
Public reporting burden for this collection is estimated to average 2 hours per response, including the time for reviewing instructions, searching existing data sources, gathering and maintaining the data needed, and completing and reviewing the collection of information. Send comments regarding this burden estimate or any other aspect of this collection of information, including suggestions for reducing this burden, to Department of Defense, Washington Headquarters Services, Directorate for Information Operations and Reports, 1215 Jefferson Davis Highway, Suite 1204, Arlington, VA 22202-4302, and to the Office of Management and Budget, Paperwork Reduction Project (0704-0188), Washington, DC 20503. PLEASE DO NOT RETURN YOUR COMPLETED FORM TO EITHER OF THESE ADDRESSES. RETURN COMPLETED FORM TO THE GOVERNMENT ISSUING CONTRACTING OFFICER FOR THE CONTRACT/ PROCURING ACTIVITY NUMBER LISTED IN ITEM 2 OF THIS FORM.			2. PROCURING ACTIVITY NO.
			3. DODAAC
4. ORIGINATOR	b. ADDRESS (Street, City, State, Zip Code) Defense Electronics Supply Center 1507 Wilmington Pike Dayton, OH 45444-5270	5. CAGE CODE 67268	6. NOR NO. 5962-R212-93
a. TYPED NAME (First, Middle Initial, Last) N. A.		7. CAGE CODE 67268	8. DOCUMENT NO. 85030
9. TITLE OF DOCUMENT MICROCIRCUIT, LINEAR, PERCISION VOLTAGE REFERENCES, THIN FILM, HYBRID.	10. REVISION LETTER		11. ECP NO.
	a. CURRENT C	b. NEW D	ECP-1
12. CONFIGURATION ITEM (OR SYSTEM) TO WHICH ECP APPLIES All			
13. DESCRIPTION OF REVISION Sheet 1: Revisions ltr column; add "D". Revisions description column; add "Changes in accordance with NOR 5962-R212-93". Revisions date column; add "93-08-17". Revision level block; delete "C" and substitute "D". Rev status of sheets; For sheets 1 and 4 delete "C" and substitute "D". Sheet 4: Table I, I _{EE} for device types 05 and 06, maximum limits column; delete "-9" and substitute "-14". Revision level block; delete "C" and substitute "D". STANDARDIZED MILITARY DRAWING APPROVAL BULLETIN: Change vendor CAGE number "51640" to "34031". (6 places).			
14. THIS SECTION FOR GOVERNMENT USE ONLY			
a. (X one)	<input checked="" type="checkbox"/>	(1) Existing document supplemented by the NOR may be used in manufacture.	
	<input type="checkbox"/>	(2) Revised document must be received before manufacturer may incorporate this change.	
	<input type="checkbox"/>	(3) Custodian of master document shall make above revision and furnish revised document.	
b. ACTIVITY AUTHORIZED TO APPROVE CHANGE FOR GOVERNMENT		c. TYPED NAME (First, Middle Initial, Last)	
d. TITLE Chief, Electronic Components Branch	e. SIGNATURE Kendall A. Cottongim		f. DATE SIGNED (YYMMDD) 93-08-17
15a. ACTIVITY ACCOMPLISHING REVISION DESC-ECT	b. REVISION COMPLETED (Signature) Gary Zahn		c. DATE SIGNED (YYMMDD) 93-08-17

REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Change to military drawing format. Page 8, table I; change group A subgroup for +V _R and -V _R ; add end-point electrical limits for +V _{RINT} and -V _{RINT} . Page 10; change output offset adjust and temperature effect for device types 03 and 04 trim circuits.	86-12-31	W. Heckman
B	Add device types 05 and 06. Inactivate case outline X. Add case outlines C, Y, and 3. Add vendors CAGE 33256 and 34707. Change drawing CAGE to 67268. Editorial changes throughout.	89-08-01	W. Heckman
C	Changed to reflect MIL-H-38534 processing. Update document. Editorial changes throughout.	92-12-22	K.A. Cottongim

THE ORIGINAL FIRST PAGE OF THIS DRAWING HAS BEEN REPLACED.

CURRENT CAGE CODE 67268

REV																		
SHEET																		
REV	C																	
SHEET	15																	
REV STATUS OF SHEETS				REV	C	C	C	C	C	C	C	C	C	C	C	C	C	
				SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14

PMIC N/A	PREPARED BY Donald R. Osborne	DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444		
STANDARDIZED MILITARY DRAWING <small>THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE</small> AMSC N/A	CHECKED BY Ray Monnin	MICROCIRCUIT, LINEAR, PRECISION VOLTAGE REFERENCES, THIN FILM, HYBRID		
	APPROVED BY William K. Heckman			
	DRAWING APPROVAL DATE 86-04-28	SIZE A	CAGE CODE 14933	85030
	REVISION LEVEL A	SHEET 1 OF 15		

1. SCOPE

1.1 Scope. This drawing describes device requirements for class H hybrid microcircuits to be processed in accordance with MIL-H-38534.

1.2 Part or Identifying Number (PIN). The complete PIN shall be as shown in the following example:



1.2.1 Device type(s). The device type(s) shall identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01, 02	2700	Precision +10.000-volt reference
03, 04	2702	Precision ±10.000-volt reference
05, 06	2701	Precision -10.000-volt reference

1.2.2 Case outline(s). The case outline(s) shall be as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
C	GDIP1-T14	14	Dual-in-line
X	See figure 1	14	Dual-in-line
Y	See figure 1	14	Dual-in-line
3	CQCC1-N28	28	Square leadless chip-carrier

1.2.3 Lead finish. The lead finish shall be as specified in MIL-H-38534. Finish letter "X" shall not be marked on the microcircuit or its packaging. The "X" designation is for use in specifications when lead finishes A, B, and C are considered acceptable and interchangeable without preference.

1.3 Absolute maximum ratings.

Supply voltage (V _S):	
V _{CC} (device types 01, 02, 03, 04)	+20 V dc
V _{EE} (device types 03, 04, 05, 06)	-20 V dc
Power dissipation (P _D), T _A = +25° C:	
Device types 01, 02, 05, 06	300 mW
Device types 03, 04	450 mW
Storage temperature range	-65° C to +150° C
Lead temperature (soldering, 10 seconds)	+300° C
Short circuit protection (to GND)	Continuous
Thermal resistance:	
Junction-to-case (θ _{JC}):	
Cases C and 3	See MIL-STD-1835
Case X	7° C/W
Case Y	8° C/W
Junction-to-ambient (θ _{JA}):	
Case X	30° C/W
Case Y	25° C/W

1.4 Recommended operating conditions.

Supply voltage range (V _{CC})	+13.5 V dc to +16.5 V dc
Supply voltage range (V _{EE})	-13.5 V dc to -16.5 V dc
Output current	5 mA ^{1/}
Ambient operating temperature range (T _A)	-55° C to +125° C

^{1/} With resistive load to pin 7 (common).

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2. APPLICABLE DOCUMENTS

2.1 Government specification and standards. Unless otherwise specified, the following specification and standards of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-H-38534 - Hybrid Microcircuits, General Specification for.

STANDARDS

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.
 MIL-STD-1835 - Microcircuit Case Outlines.

(Copies of the specification and standards required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-H-38534 and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-H-38534 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.2.3 Trim circuits. The trim circuits shall be as specified on figure 3.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-H-38534. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in QML-38534 (see 6.6 herein).

3.6 Manufacturer eligibility. In addition to the general requirements of MIL-H-38534, the manufacturer of the part described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, produced on the certified line, for each device type listed herein. The data should also include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DESC-EC) upon request.

3.7 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in QML-38534 (see 6.6 herein). The certificate of compliance submitted to DESC-EC prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-H-38534 and the requirements herein.

3.8 Certificate of conformance. A certificate of conformance as required in MIL-H-38534 shall be provided with each lot of microcircuits delivered to this drawing.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions 1/ -55° C ≤ T _A ≤ +125° C unless otherwise specified		Group A subgroups	Device type	Limits		Unit
						Min	Max	
Selection output error 2/	+V _R	+10.000 V output		4	01	-5.0	+5.0	mV
				5,6		-8.0	+8.0	
		+10.000 V output		4	02,04	-2.5	+2.5	
	5,6			-5.5		+5.5		
	+10.000 V output		4	03	-5.0	+5.0		
			5,6		-10	+10		
-V _R	-10.000 V output		4	03,05	+5.0	-5.0		
			5,6		+10	-10		
	-10.000 V output		4	04,06	+2.5	-2.5		
5,6			+5.5		-5.5			
Interim output error	+V _{RINT}	+10.000 V output T _A = +25° C		1	01,02, 03,04	Initial	+5	
						End-point	-10	+10
	-V _{RINT}	-10.000 V output T _A = +25° C		1	03,04, 05,06	Initial	-5	
						End-point	+10	-10
Output adjust range for trim circuits (see figure 3)	+V _{RADJ}	+10.000 V output, T _A = +25° C		1	01,02, 03,04	-20	+20	
	-V _{RADJ}	-10.000 V output, T _A = +25° C		1	03,04, 05,06	+20	-20	
Quiescent current I _{CC}	V _{CC} = +15 V	no load, T _A = +25° C		1	01,02		+14	
		V _{CC} = +15 V V _{EE} = -15 V, no load T _A = +25° C		1	03,04		+17	
	I _{EE}	V _{CC} = +15 V, no load V _{EE} = -15 V T _A = +25° C		1	03,04		-4	
		V _{EE} = -15 V, no load T _A = +25° C		1	05,06		-9	

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions ^{1/} -55° C ≤ T _A ≤ +125° C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Line regulation	+V _{RL}	+10 V output, T _A = +25° C, +13.5 V ≤ V _{CC} ≤ +16.5 V	1	01,02, 03,04	-900	+900	μV
	-V _{RL}	-10.000 V output, T _A = +25° C, -13.5 V ≤ V _{EE} ≤ -16.5 V	1	03,04, 05,06	+900	-900	
Load regulation	+V _{RLOAD}	+10.000 V output, T _A = +25° C, 0 ≤ I _L ≤ 10 mA	1	01,02, 03,04	-500	+500	μV
	-V _{RLOAD}	-10.000 V output, T _A = +25° C, 0 ≤ I _L ≤ 10 mA	1	03,04, 05,06	+500	-500	
Output current	+I _L		1	01,02, 03,04	10		mA
			2,3		5		
	-I _L		1	03,04, 05,06	10		
			2,3		5		
Output noise ^{3/}		V _{OUT} = 10 V, no load, 0.1 Hz ≤ BW ≤ 10 Hz, T _A = +25° C	4	01,02, 03,04, 05,06		150	μV/ p-p

^{1/} Unless otherwise specified, V_{IN} = +15 V, R_L = 2 kΩ to pin 7 (common), all tests after 3 minutes warm-up period.

^{2/} Output voltage change as a function of temperature is determined using the box method. Each device is tested at -55° C, +25° C, and +125° C. At each temperature the output voltage (V_{OUT}) must fall within the rectangular area bounded by the minimum and maximum temperatures. This method gives a maximum temperature coefficient of 9 ppm/° C and a typical value of 3 ppm/° C.

^{3/} Parameter shall be tested as part of device initial characterization and after design and process changes. Parameter shall be guaranteed to limits specified in table I for all lots not specifically tested.

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Case outline X.

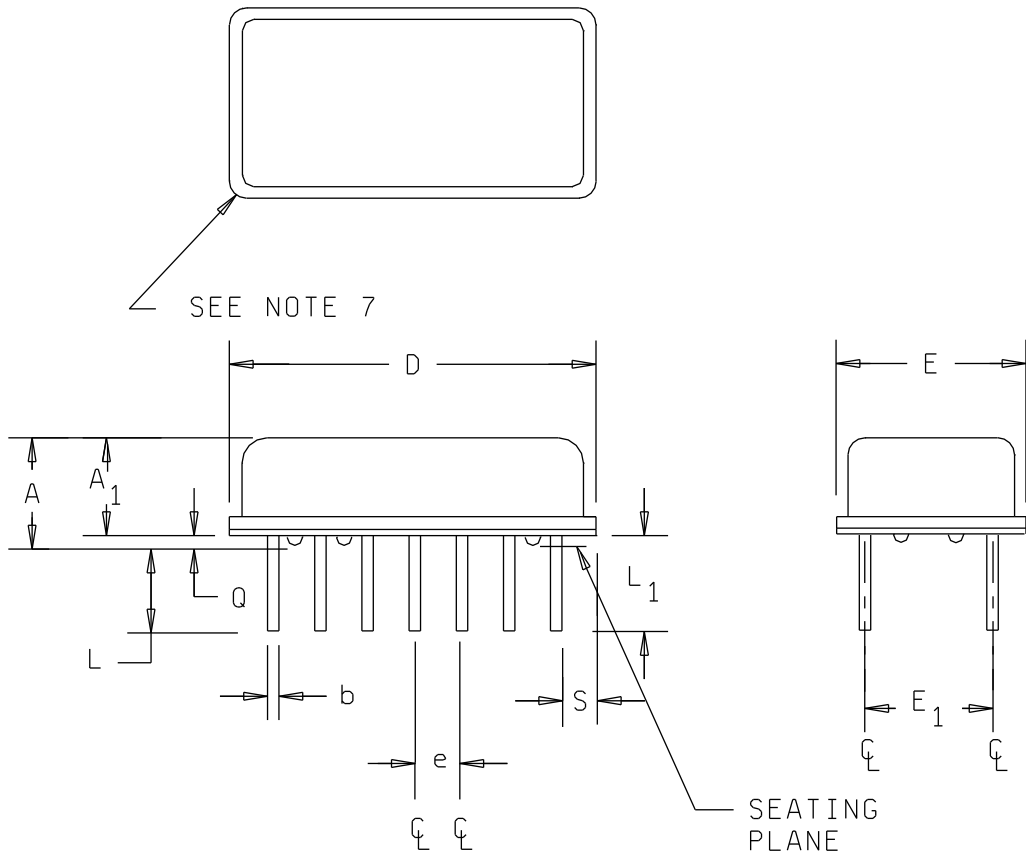


FIGURE 1. Case outline(s).

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A

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REVISION LEVEL
C

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Case outline X - Continued.

Dimensions					
Symbol	Inches		Millimeters		Notes
	Min	Max	Min	Max	
A	.215	.250	5.46	6.35	
A ₁	.175	.215	4.44	5.46	
b	.016	.020	0.41	0.51	8
φ _b	.016	.020	0.41	0.51	4
D	.860	.885	21.84	22.48	4
E	.490	.520	12.45	13.21	7
E ₁	.295	.305	7.49	7.75	
e	.100 BSC		2.54 BSC		5, 8
L	.130	.255	3.30	6.48	
L ₁	.150	.290	3.81	7.37	
Q	.020	.035	0.51	0.89	3
S	.100	.160	2.54	4.07	
S ₁	.080	.180	2.03	4.57	
α	0°	15°	0°	15°	

NOTES:

1. Index area: A notch, square-package corner, or a pin one index point shall be located adjacent to pin one and within the shaded area shown. The manufacturer's identification shall not be used as a pin one identification mark.
2. Dimension Q shall be measured from the seating plane to the base plane.
3. This dimension allows for off-center lid, meniscus, and weld squash.
4. The basic pin spacing is .100 inch (2.54 mm) between centerlines. Each pin centerline shall be located within ±.010 inch (0.25 mm) of its exact longitudinal position relative to pins 1 and 14.
5. Dimension S₁ is not used.
6. Lead center when a = 0°. E₁ shall be measured at the centerline of the leads (see MIL-STD-1835).
7. All leads: Increase maximum limit by .003 inch (0.08 mm) measured at the widest diameter when lead finish A or B is applied.
8. If this configuration is used, no polymer or organic materials shall be applied or molded to the bottom of the package or cover the leads.

FIGURE 1. Case outline(s) - Continued.

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Case outline Y.

14	8
1	7

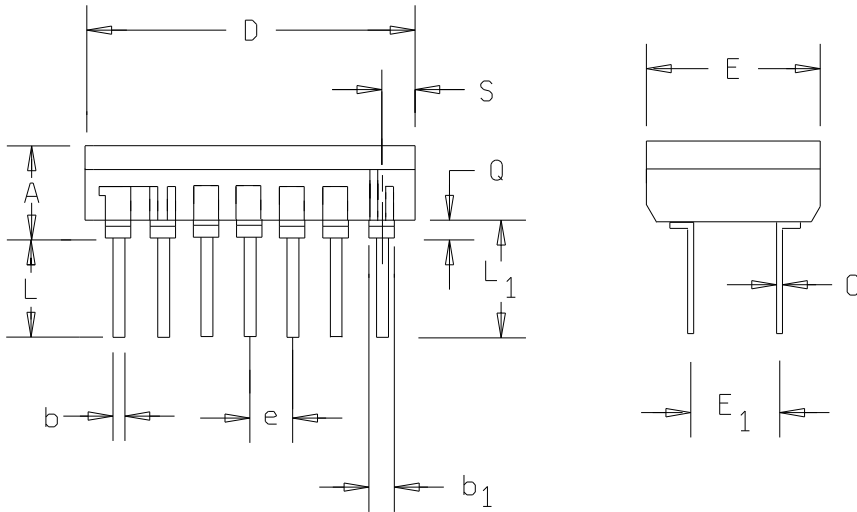


FIGURE 1. Case outline(s) - Continued.

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A

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REVISION LEVEL
C

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Case outline Y - Continued.

Dimensions					
Symbol	Inches		Millimeters		Notes
	Min	Max	Min	Max	
A	.140	.200	3.56	5.08	
b	.014	.023	0.36	0.58	
b ₁	.030	.070	0.76	1.78	2
c	.008	.015	0.20	0.38	
D	.770	.810	19.56	20.57	
E	.480	.510	12.19	12.95	
E ₁	.295	.305	7.49	7.75	6
e	0.100 BSC		2.54 BSC		4, 7
L	.150	.200	3.81	5.08	
L ₁	.180	---	4.57	---	
Q	.015	.035	0.38	0.89	3
S	---	.137	---	3.48	5
S ₁	.060	---	1.52	---	5

NOTES:

1. Index area: A notch or a lead one identification mark is located adjacent to lead one.
2. The minimum limit for dimension b₁ may be .023 inch (0.58 mm) for all four corner leads only.
3. Dimension Q shall be measured from the seating plane to the base plane.
4. The basic pin spacing is .100 inch (2.54 mm) between centerlines.
5. Applies to all four corners.
6. E₁ shall be measured at the centerline of all the leads.

FIGURE 1. Case outline(s) - Continued.

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Device types 01 and 02.

2700		
Terminal number	Terminal symbol	
Case outlines	C, X, Y	Z
1	NC	V _O SEN
2	NC	-IN
3	NC	REF GND
4	NC	GND SEN
5	NC	NC
6	NC	NC
7	GND	NC
8	NC	NC
9	NC	NC
10	TEST POINT	NC
11	V _{CC}	NC
12	+10 V ADJ	NC
13	+10 V OUT	NC
14	+10 V ADJ	NC
15		NC
16		NC
17		NC
18		V _Z
19		ZENER GND
20		NC
21		NC
22		V _{OS}
23		V _{OS}
24		POWER GND
25		V _{CC}
26		+IN
27		V _O FORCE
28		+10 V REF OUT

Device types 03 and 04.

2702		
Terminal number	Terminal symbol	
Case outlines	C, X, Y	Z
1	-10 V ADJ	V _O SEN
2	-10 V OUT	-IN
3	-10 V ADJ	REF GND
4	V _{EE}	GND SEN
5	NC	NC
6	NC	NC
7	GND	-V _{OS}
8	NC	-V _{OS}
9	NC	V _{EE}
10	TEST POINT	+IN
11	V _{CC}	V _{CC}
12	+10 V ADJ	NC
13	+10 V OUT	-IN
14	+10 V ADJ	V _O FORCE
15		-10 V REF OUT
16		V _O SENSE
17		+10 V IN
18		V _Z
19		ZENER GND
20		NC
21		NC
22		V _{OS}
23		V _{OS}
24		POWER GND
25		V _{CC}
26		+IN
27		V _O FORCE
28		+10 V REF OUT

FIGURE 2. Terminal connections.

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Device types 05 and 06.

2701		
Terminal number	Terminal symbol	
Case outlines	C, X, Y	Z
1	NC	NC
2	NC	NC
3	NC	REF GND
4	NC	GND SENSE
5	NC	NC
6	NC	NC
7	GND	-V _{OS}
8	NC	-V _{OS}
9	NC	V _{EE}
10	TEST POINT	+IN
11	V _{EE}	NC
12	-10 V ADJ	PWR GND
13	-10 V OUT	-IN
14	-10 V ADJ	V _O FORCE
15		V _O SENSE
16		V _O SENSE
17		NC
18		V _Z
19		ZENER GND
20		NC
21		NC
22		NC
23		NC
24		NC
25		NC
26		NC
27		NC
28		NC

FIGURE 2. Terminal connections - Continued.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		85030
		REVISION LEVEL C	SHEET 11

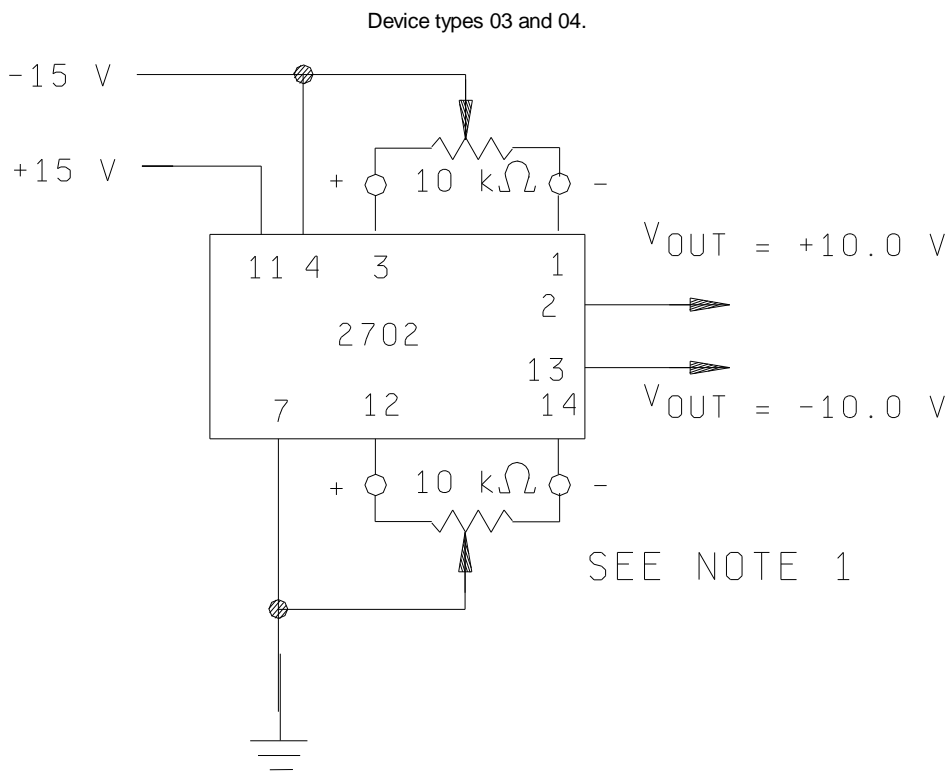
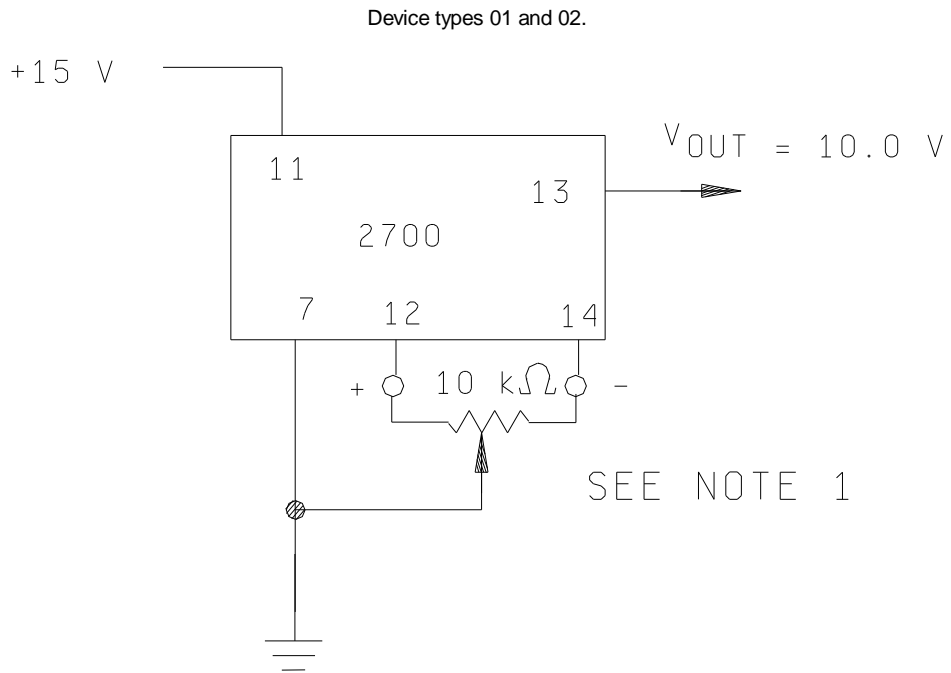


FIGURE 3. Trim circuit(s).

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DAYTON, OHIO 45444

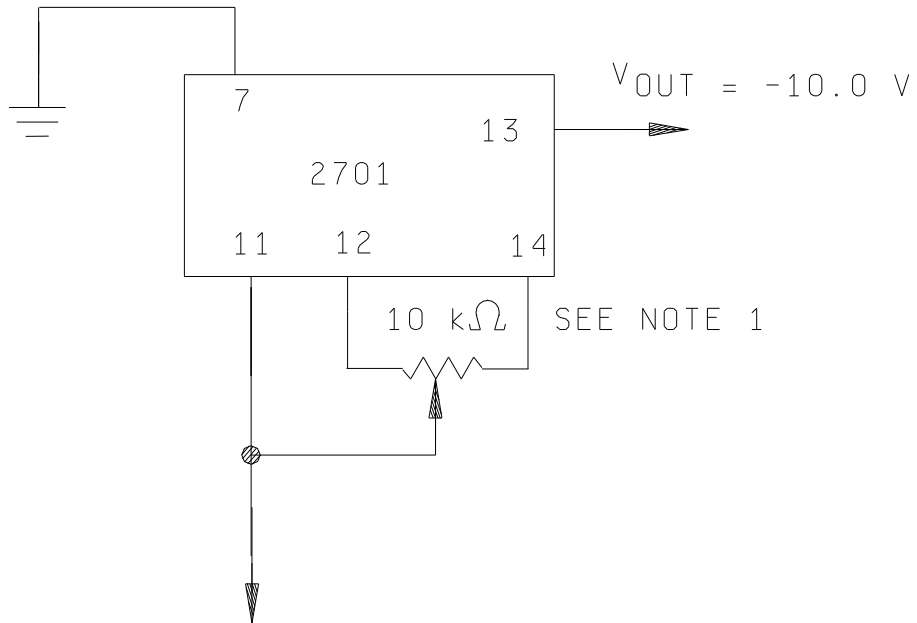
SIZE
A

85030

REVISION LEVEL
C

SHEET
12

Device types 05 and 06.



NOTES:

1. External 10 kΩ potentiometer provides a $\pm 20 \text{ mV}$ minimum output offset adjust. Temperature effect is $4 \mu\text{V}/^\circ\text{C}$ per mV of offset correction (external adjustment optional).
2. Dual-in-line package only.

FIGURE 3. Trim circuit(s) - Continued.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (in accordance with method 5008, group test table)
Interim electrical parameters	---
Final electrical test parameters	1*,2,3,4
Group A test requirements	1,2,3,4,5,6
Group C end-point electrical parameters	1

* PDA applies to subgroup 1.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-H-38534.

4.2 Screening. Screening shall be in accordance with MIL-H-38534. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DESC-EC or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.

(2) T_A as specified in accordance with table I of method 1015 of MIL-STD-883.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-H-38534 and as specified herein.

4.3.1 Group A inspection. Group A inspection shall be in accordance with MIL-H-38534 and as follows:

a. Tests shall be as specified in table II herein.

b. Subgroups 7, 8, 9, 10, and 11 shall be omitted.

4.3.2 Group B inspection. Group B inspection shall be in accordance with MIL-H-38534.

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4.3.3 Group C inspection. Group C inspection shall be in accordance with MIL-H-38534 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DESC-EC or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
 - (2) T_A as specified in accordance with table I of method 1005 of MIL-STD-883.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 Group D inspection. Group D inspection shall be in accordance with MIL-H-38534.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-H-38534.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-EC, telephone (513) 296-6047.

6.5 Comments. Comments on this drawing should be directed to DESC-EC, Dayton, Ohio 45444, or telephone (513) 296-5373.

6.6 Approved sources of supply. Approved sources of supply are listed in QML-38534. Additional sources will be added to QML-38534 as they become available. The vendors listed in QML-38534 have agreed to this drawing and a certificate of compliance (see 3.7 herein) has been submitted to and accepted by DESC-EC.

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STANDARDIZED MILITARY DRAWING SOURCE APPROVAL BULLETIN

DATE: 92-12-22

Approved sources of supply for SMD 85030 are listed below for immediate acquisition only and shall be added to QML-38534 during the next revision. QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DESC-EC. This bulletin is superseded by the next dated revision of QML-38534.

Standardized military drawing PIN	Vendor CAGE number	Vendor similar PIN <u>1/</u>
8503001CX	33256	HS2700SD/883B
8503001XX	<u>2/</u>	2700SD/883B
8503001YX	51640	2700SD/883B
8503001YX	34707	HC2700SD/883B
85030013X	34707	HC2700SLCC/883B
8503002CX	33256	HS2700UD/883B
8503002XX	<u>2/</u>	2700UD/883B
8503002YX	51640	2700UD/883B
8503002YX	34707	HC2700UD/883B
85030023X	34707	HC2700ULCC/883B
8503003CX	33256	HS2702SD/883B
8503003XX	<u>2/</u>	2702SD/883B
8503003YX	51640	2702SD/883B
8503003YX	34707	HC2702SD/883B
85030033X	34707	HC2702SLCC/883B
8503004CX	33256	HS2702UD/883B
8503004XX	<u>2/</u>	2702UD/883B
8503004YX	51640	2702UD/883B
8503004YX	34707	HC2702UD/883B
85030043X	34707	HC2702ULCC/883B
8503005CX	33256	HS2701SD/883B
8503005YX	51640	2701SD/883B
8503005YX	34707	HC2701SD/883B
85030053X	34707	HC2701SLCC/883B
8503006CX	33256	HS2701UD/883B
8503006YX	51640	2701UD/883B
8503006YX	34707	HC2701UD/883B
85030063X	34707	HC2701ULCC/883B

- 1/ **Caution.** Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 2/ Inactive for new design, no longer available from manufacturer.

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STANDARDIZED MILITARY DRAWING SOURCE APPROVAL BULLETIN - Continued.

DATE: 92-12-22

<u>Vendor CAGE number</u>	<u>Vendor name and address</u>
33256	Sipex Corporation Hybrid Systems Division 22 Linnell Circle Billerica, CA 01821
34031	Analog Devices, Incorporated Computer Labs Group 7910 Triad Center Drive Greensboro, NC 27409 Point of contact: Assembled Parts Division 7910 Triad Center Drive Greensboro, NC 27409
34707	Hycomp, Incorporated 165 Cedar Hill Street Marlborough, MA 01752

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in this information bulletin.

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