

REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED

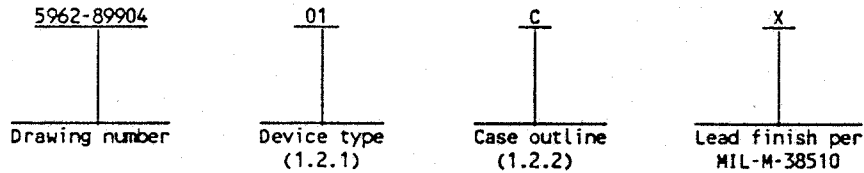
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PMIC N/A	PREPARED BY <i>Rick C. Offner</i>	DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444		
STANDARDIZED MILITARY DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE AMSC N/A	CHECKED BY <i>Charles E. Besore</i>	MICROCIRCUIT, LINEAR, QUAD, LOW POWER PRECISION COMPARATOR, MONOLITHIC SILICON		
	APPROVED BY <i>Monica L. Pelley</i>	SIZE A	CAGE CODE 67268	5962-89904
	DRAWING APPROVAL DATE 27 APRIL 1990	SHEET 1 OF 10		
REVISION LEVEL				

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device types. The device types shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	CMP404A	Quad low-power precision comparator
02	CMP404B	Quad low-power precision comparator
03	CMP04B	Quad low-power precision comparator

1.2.2 Case outline. The case outline shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
C	D-1 (14-lead, .785" x .310" x .200"), dual-in-line package

1.3 Absolute maximum ratings.

Supply voltage	36 V or ±18 V
Input voltage	-0.3 V to V+
Output voltage	-0.3 V to 36 V
Power dissipation	500 mW ^{1/}
Input current	20 mA ^{2/}
Output short-circuit to V+	50 mA ^{3/}
Lead temperature (soldering, 60 seconds)	+300°C
Operating temperature range	-55°C to +125°C
Storage temperature range	-65°C to +150°C
Thermal resistance, junction-to-case (θ_{JC})	See MIL-M-38510, appendix C
Thermal resistance, junction-to-ambient (θ_{JA})	100°C/W

1.4 Recommended operating conditions.

Supply voltage (V+)	5 V
Ambient operating temperature range (T_A)	-55°C to +125°C

1/ Derate at T_A above +100°C by 10 mW/°C.

2/ Limit for input current that flows when input voltage signals exceed V+ or GND biasing internal junctions.

3/ Short circuits to V+ can cause excessive heating and eventual destruction. The maximum output current is 50 mA.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89904
	REVISION LEVEL	SHEET 2

2. APPLICABLE DOCUMENTS

2.1 Government specification, standard, and bulletin. Unless otherwise specified, the following specification, standard, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

BULLETIN

MILITARY

MIL-BUL-103 - List of Standardized Military Drawings (SMD's).

(Copies of the specification, standard, and bulletin required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Case outline. The case outline shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in MIL-BUL-103 (see 6.6 herein).

STANDARDIZED MILITARY DRAWING

DEFENSE ELECTRONICS SUPPLY CENTER
DAYTON, OHIO 45444

SIZE
A

5962-89904

REVISION LEVEL

SHEET 3

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $V_+ = 5\text{ V}$ $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$ unless otherwise specified	Device type	Group A subgroups	Limits		Unit				
					Min	Max					
Input offset voltage	V_{OS}	$R_S = 500, V_{CM} \leq 3.5\text{ V}$	01, 03	1		1.0	mV				
				2, 3		2.0					
			02	1		2.0					
				2, 3		3.0					
Input offset current	I_{OS}	$I_{IN(+)} - I_{IN(-)}$	01	1		10.0	nA				
				2, 3		50.0					
			02	1		25.0					
				2, 3		100.0					
			03	1		10.0					
				2, 3		20.0					
			Input bias current	I_B	$I_{IN(+)} \text{ or } I_{IN(-)}$	01		1		50.0	nA
								2, 3		100.0	
02, 03	1					100.0					
	2, 3					200.0					
Supply current	I+	$R_L = \infty$	01	1,2,3		300.0	μA				
		$V_+ = 36\text{ V}, R_L = \infty$				500.0					
		$R_L = \infty$	02	1,2,3		350.0					
		$V_+ = 36\text{ V}, R_L = \infty$				550.0					
		$V_+ = 30\text{ V}, R_L = \infty$	03	1		2.0		mA			
					2, 3				3.0		

See footnotes at end of table.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89904
		REVISION LEVEL	SHEET 4

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions V+ = 5 V -55°C ≤ T _A ≤ +125°C unless otherwise specified	Device type	Group A subgroups	Limits		Unit
					Min	Max	
Common-mode rejection	CMR	R _L = 15 kΩ, V _{CM} = 0 V to 3.5 V	01	1,2,3	75.0		dB
		R _L = 15 kΩ, V _{CM} = 0 V to 3.0 V	02	1	75.0		
				2, 3	65.0		
		V+ = 15 V, R _L = 15 kΩ V _{CM} = 1.5 V to 13.5 V	03	1	80.0		
				2, 3	60.5		
Common-mode voltage range 1/	CMVR		01, 02	1		3.5	mV
				2, 3		3.0	
		V+ = 15 V	03	1		13.5	
				2, 3		13.0	
Power supply rejection	PSR	V+ = 5 V to 30 V, R _L = 15 kΩ	01	1, 2, 3	75.0		dB
			02		65.0		
		V+ = 5 V to 18 V R _L = 15 kΩ	03	1, 2, 3	80.0		
Output sink current	I _{SINK}	V _{IN} ⁺ = 0 V, V _{IN} ⁻ = 1 V, V _O = 2 V	01, 02	1, 2, 3	10.0		mA
		V _{IN} ⁺ = 0 V, V _{IN} ⁻ = 1 V, V _O = 1.5 V	03	1	6.0		
				2, 3	5.0		
Saturation voltage	V _{OL}	V _{IN} ⁺ = 0 V, V _{IN} ⁻ = 1 V I _{SINK} = 1 mA	01, 02	1		400.0	mV
				2, 3		500.0	
		V _{IN} ⁺ = 0 V, V _{IN} ⁻ = 1 V I _{SINK} = 4 mA	03	1		400.0	
				2, 3		700.0	

See footnote at end of table.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89904
	REVISION LEVEL	SHEET 5

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $V_+ = 5\text{ V}$ $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$ unless otherwise specified	Device type	Group A subgroups	Limits		Unit
					Min	Max	
Output leakage current	I_{LEAK}	$V_{\text{IN}+} \geq 1\text{ V},$ $V_{\text{IN}-} = 0\text{ V},$ $V_{\text{O}} = 30\text{ V}$	01, 02	1		0.1	μA
				2, 3		0.4	
			03	1		0.1	
				2, 3		0.2	
Voltage gain	A_V	$R_L = 15\text{ k}\Omega,$ 2/ $\Delta V_{\text{O}} = 2.4\text{ V}$	01, 02	1, 2, 3	50		V/mV
			03			70	
Large-signal response time	t_r	$V_{\text{IN}} = \text{TTL logic swing},$ $V_{\text{REF}} = 1.4\text{ V},$ $R_L = 5.1\text{ k}\Omega$	01, 02	9		1.5	μs
				10, 11		1.8	
		5 V input step, $V_{\text{REF}} = 1.4\text{ V}, V_{\text{RL}} = 5\text{ V}$ $R_L = 5.1\text{ k}\Omega$	03	9		0.7	
				10, 11		1.4	
Small-signal response time	t_r	Low to high transition, $V_{\text{RL}} = 5\text{ V}, R_L = 5.1\text{ k}\Omega,$ 100 mV input step, 5 mV overdrive	01,02	9,10,11		1.5	μs
			03		9,10,11		
		High to low transition, $V_{\text{RL}} = 5\text{ V}, R_L = 5.1\text{ k}\Omega$ 100 mV input step, 5 mV overdrive	01,02	9,10,11		3.5	
			03		9,10,11		

1/ CMVR is guaranteed by CMR test conditions. For device 03 the input common-mode voltage, or either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is supply voltage -1.5 V, but either or both inputs can go to 30 V without damage.

2/ If not tested, shall be guaranteed to the limits specified in table I herein for device type 03 only.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89904
		REVISION LEVEL	SHEET 6

Device types	01, 02, and 03
Case outline	C
Terminal number	Terminal symbol
1	OUT2
2	OUT1
3	V+
4	IN1-
5	IN+
6	IN2-
7	IN2+
8	IN3-
9	IN3+
10	IN4-
11	IN4+
12	GND
13	OUT4
14	OUT3

FIGURE 1. Terminal connections.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89904
	REVISION LEVEL	SHEET 7

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.6 herein). The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.9 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) $T_A = +125^\circ\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, 6, 7 and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test conditions, method 1005 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) $T_A = +125^\circ\text{C}$, minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89904
	REVISION LEVEL	SHEET 8

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table 1)
Interim electrical parameters (method 5004)	1
Final electrical test parameters (method 5004)	1, 2, 3
Group A test requirements (method 5005)	1, 2, 3, 9, 10, 11
Group C and D end-point electrical parameters (method 5005)	1

*PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-ECS, telephone (513) 296-6022.

6.5 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone (513) 296-5375.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89904
	REVISION LEVEL	SHEET 9

6.6 Approved source of supply. An approved source of supply is listed in MIL-BUL-103. Additional sources will be added to MIL-BUL-103 as they become available. The vendor listed in MIL-BUL-103 has agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-ECS. The approved source of supply listed below is for information purposes only and is current only to the date of the last action of this document.

Military drawing part number	Vendor CAGE number	Vendor similar part number 1/
5962-8990401CX	06665	CMP404AY
5962-8990402CX	06665	CMP404BY
5962-8990403CX	06665	CMP04BY

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

Vendor name and address

06665

Precision Monolithics, Incorporation
 1500 Space Park Drive
 P.O. Box 58020
 Santa Clara, CA 95050-8020

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89904
	REVISION LEVEL	SHEET 10